

FIG. 4

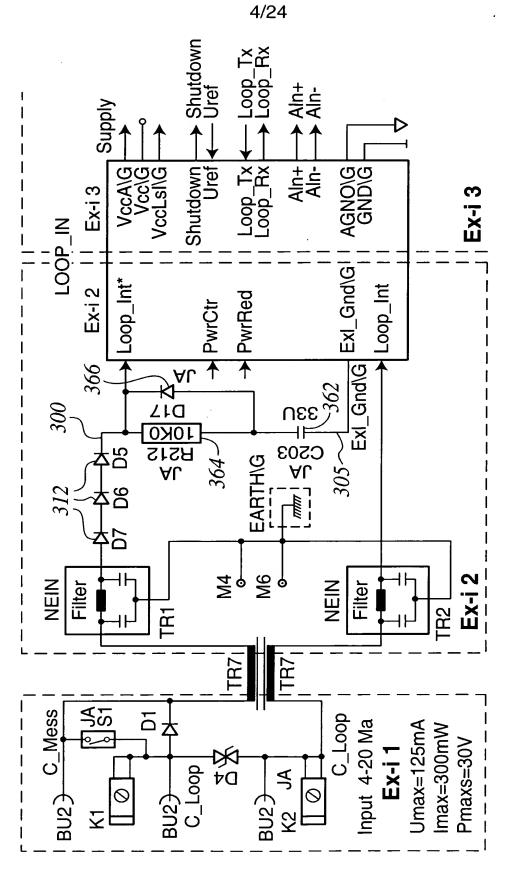
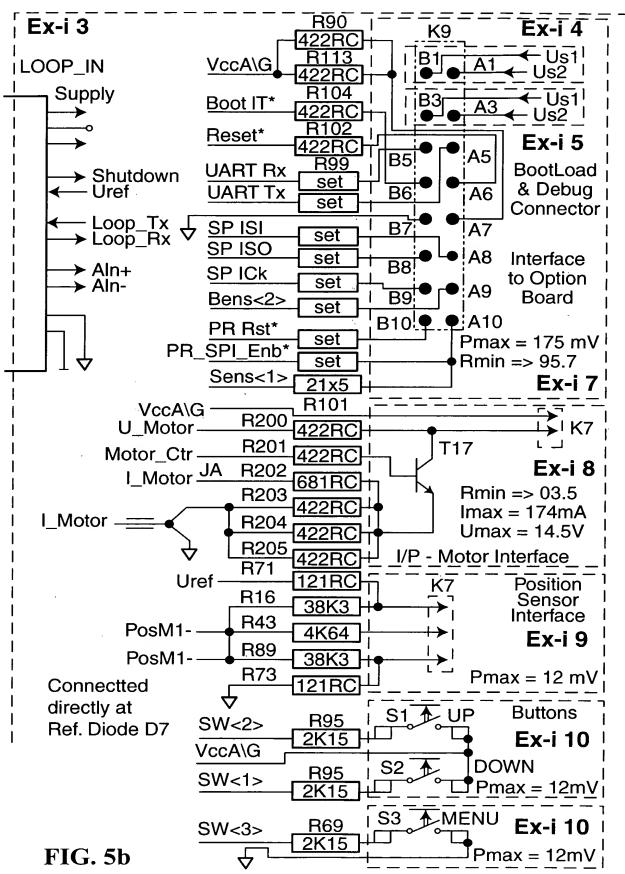


FIG. 5a



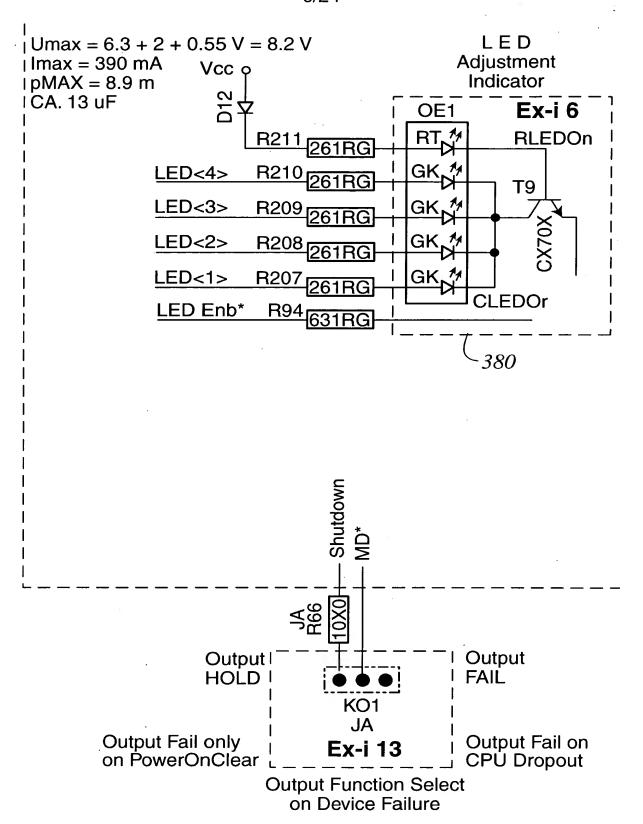
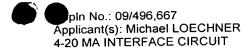


FIG. 5c



## **Terminatorfield**

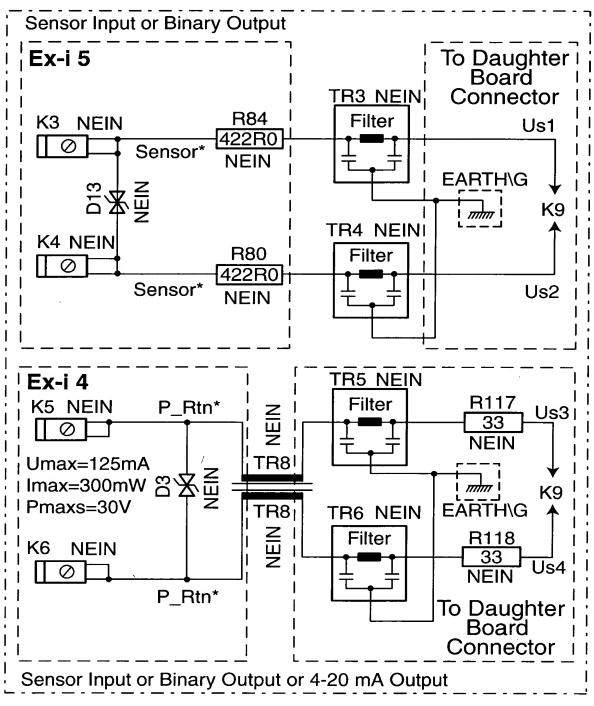
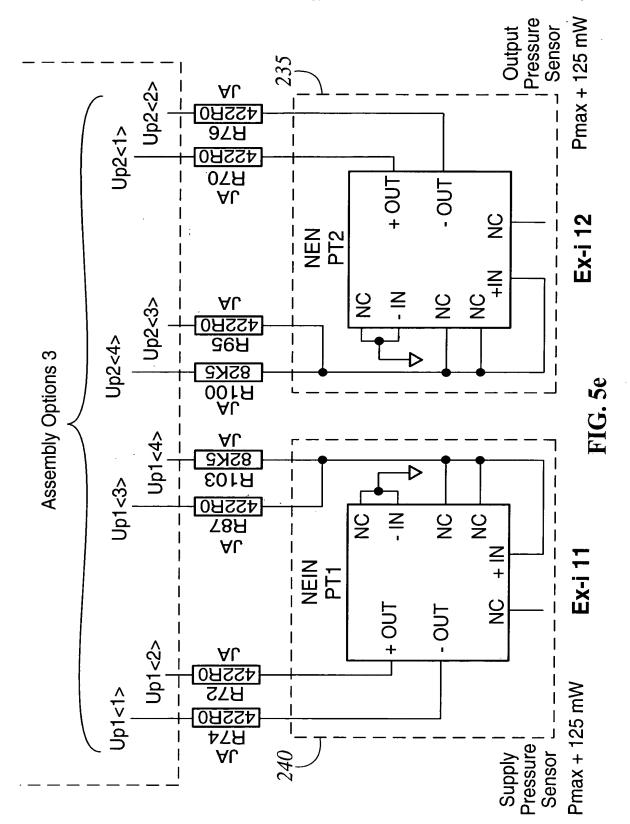
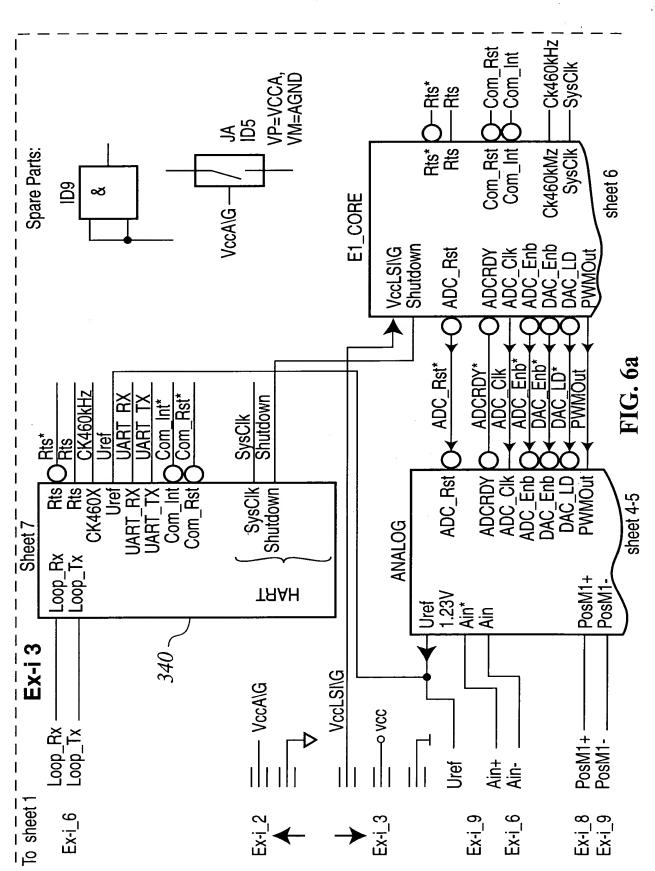


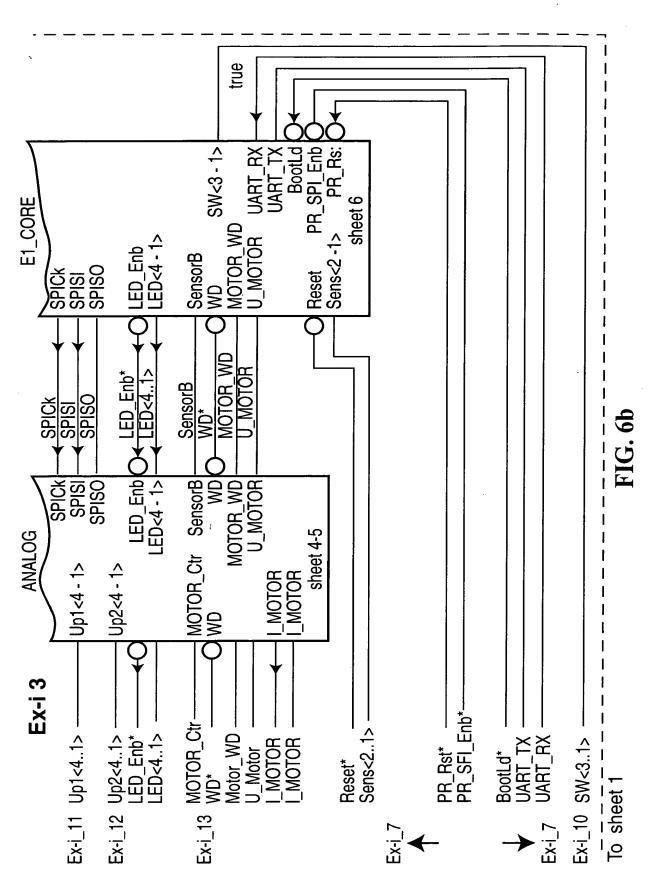
FIG. 5d



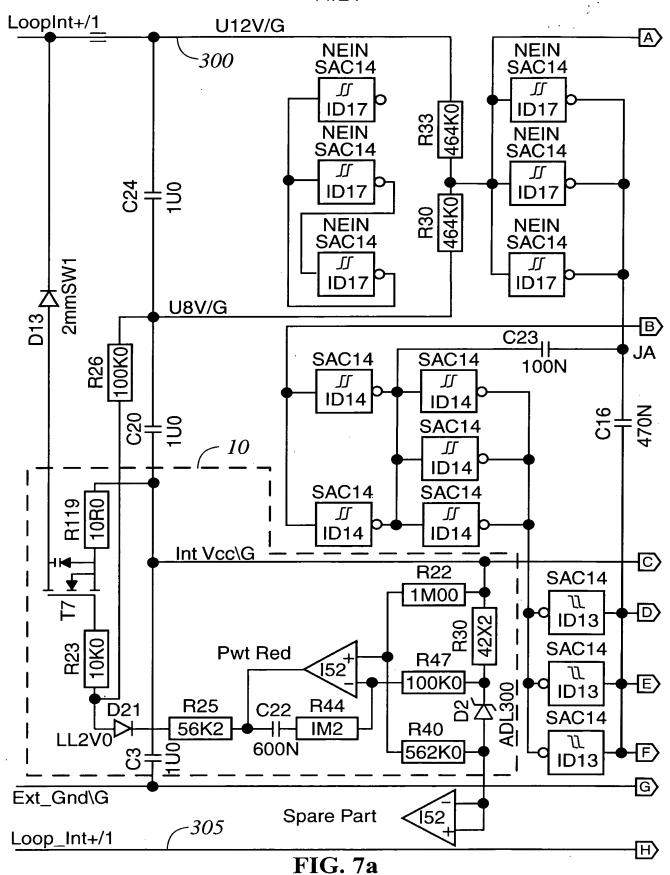




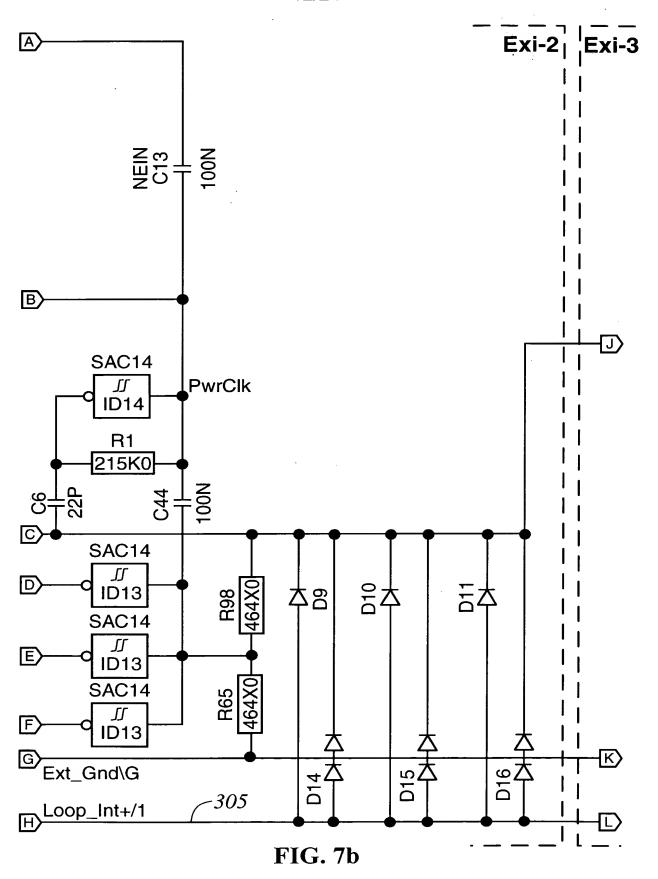








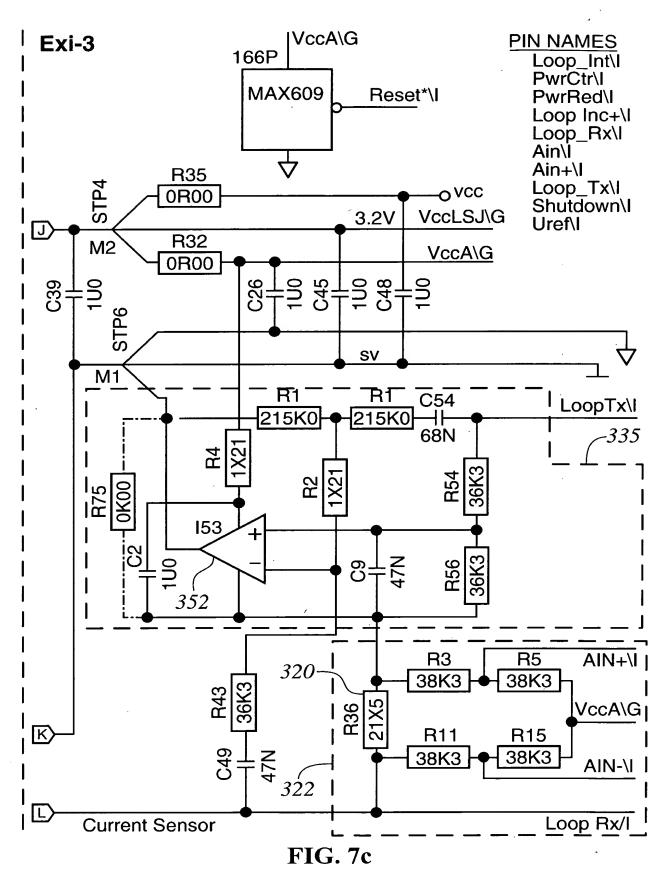




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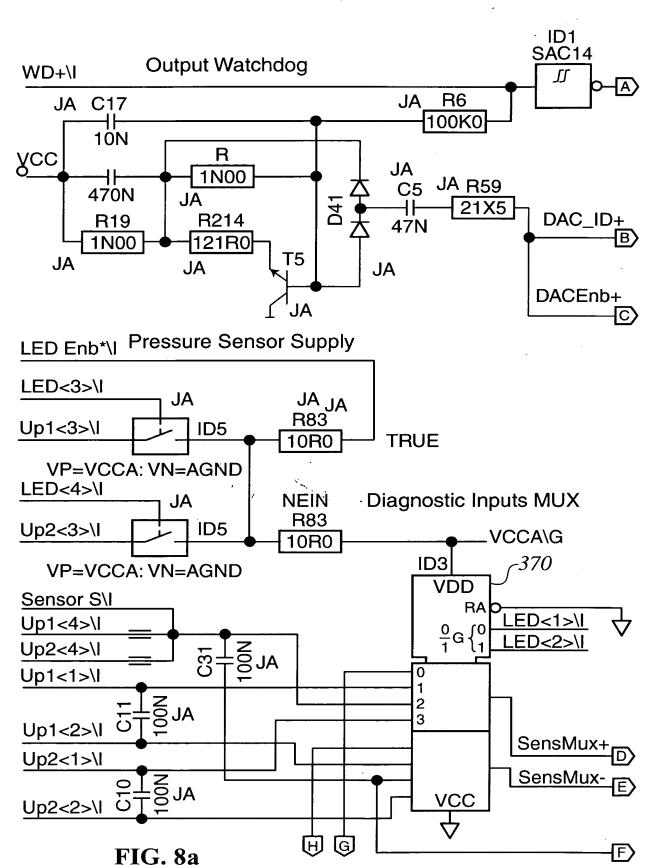
Applicant(s): Michael LOECHNER 4-20 MA INTERFACE CIRCUIT



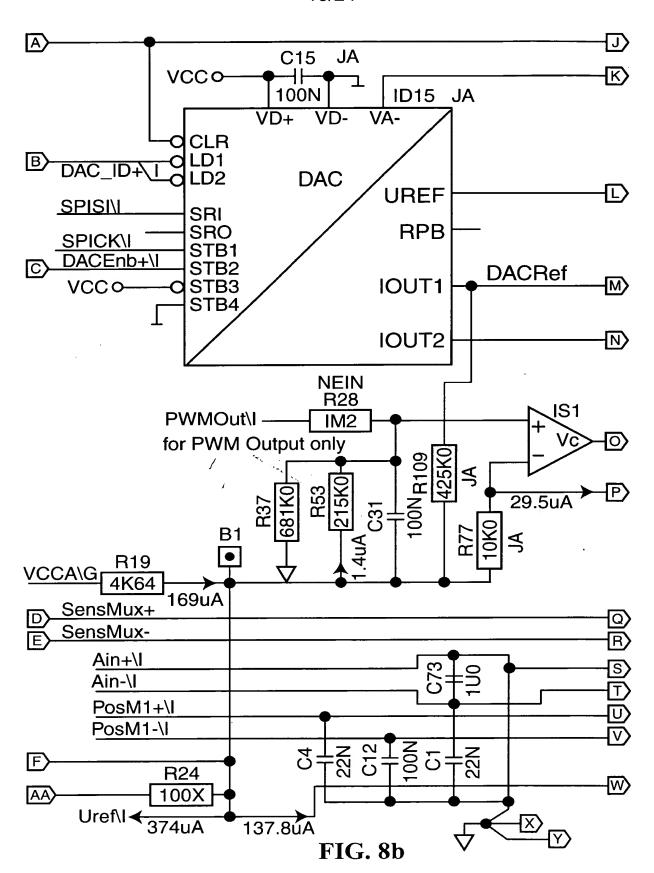






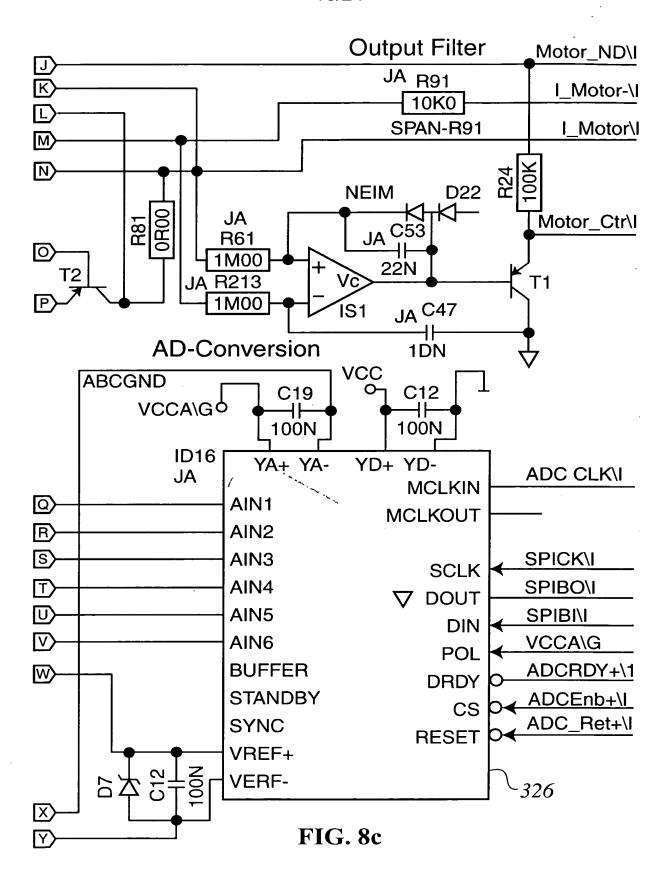


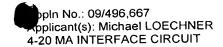




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Applicant(s): Michael LOECHNER
4-20 MA INTERFACE CIRCUIT

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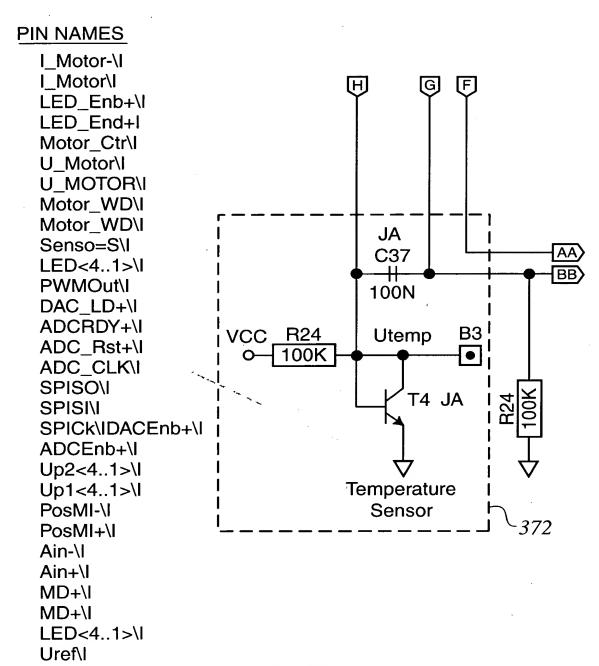
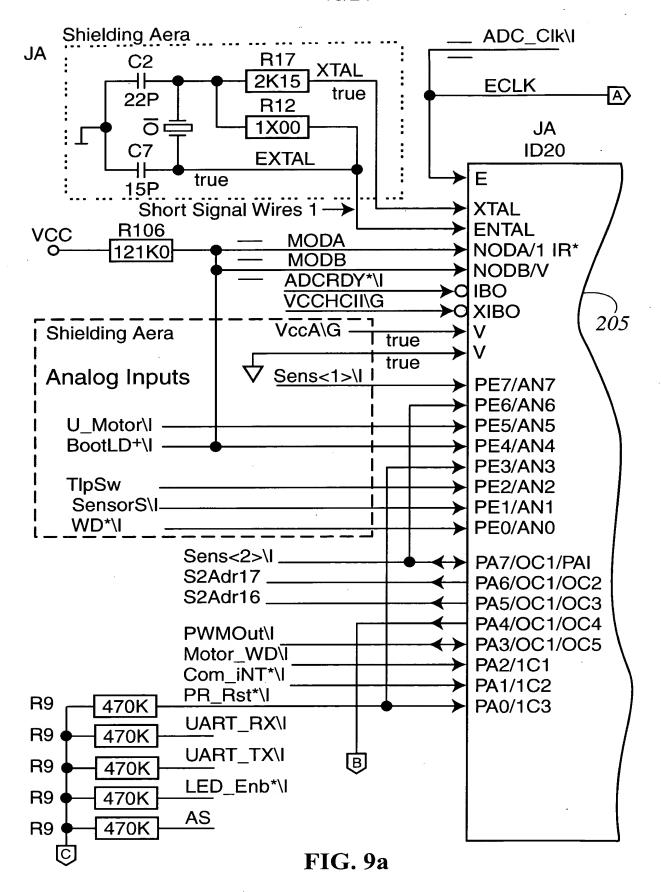
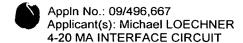
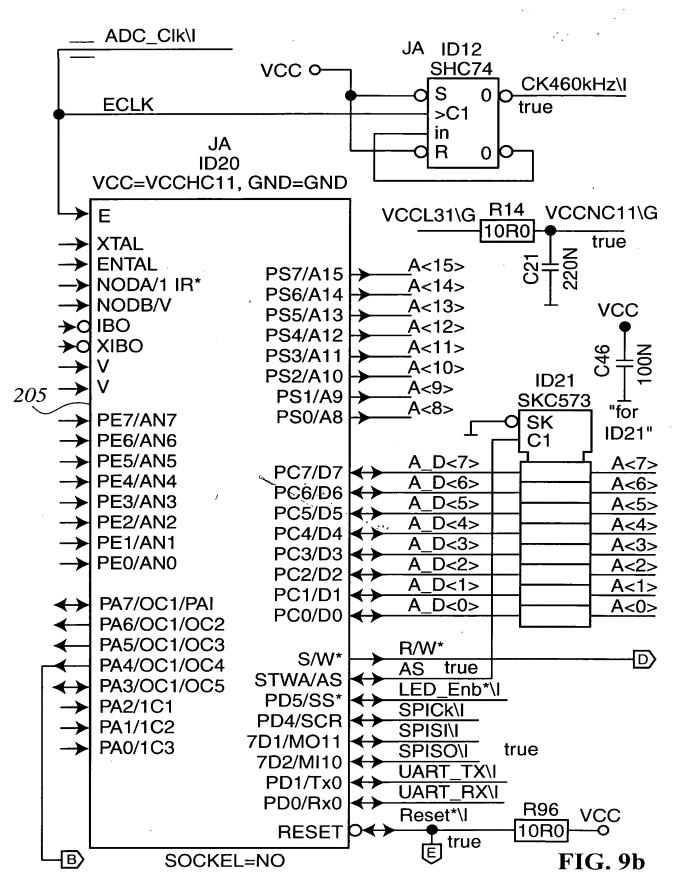


FIG. 8d

pln No.: 09/496,667 Applicant(s): Michael LOECHNER 4-20 MA INTERFACE CIRCUIT







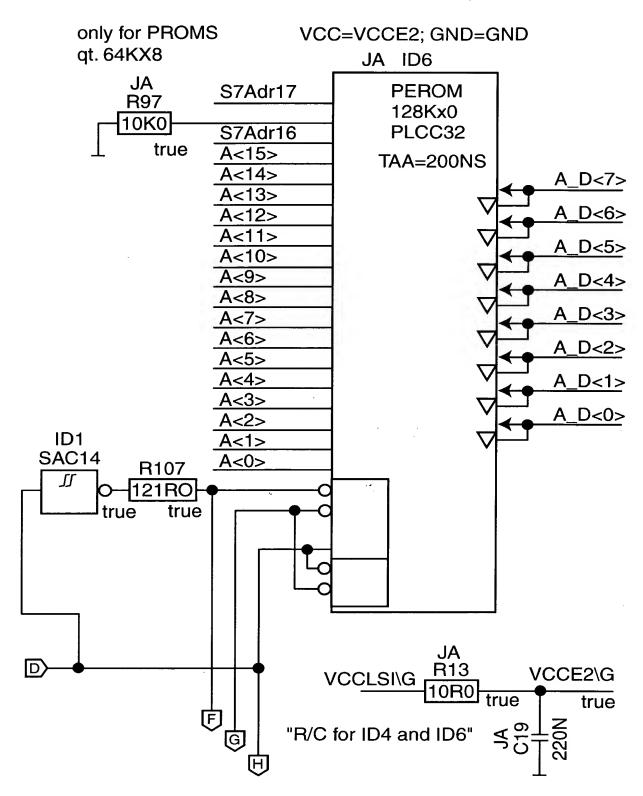
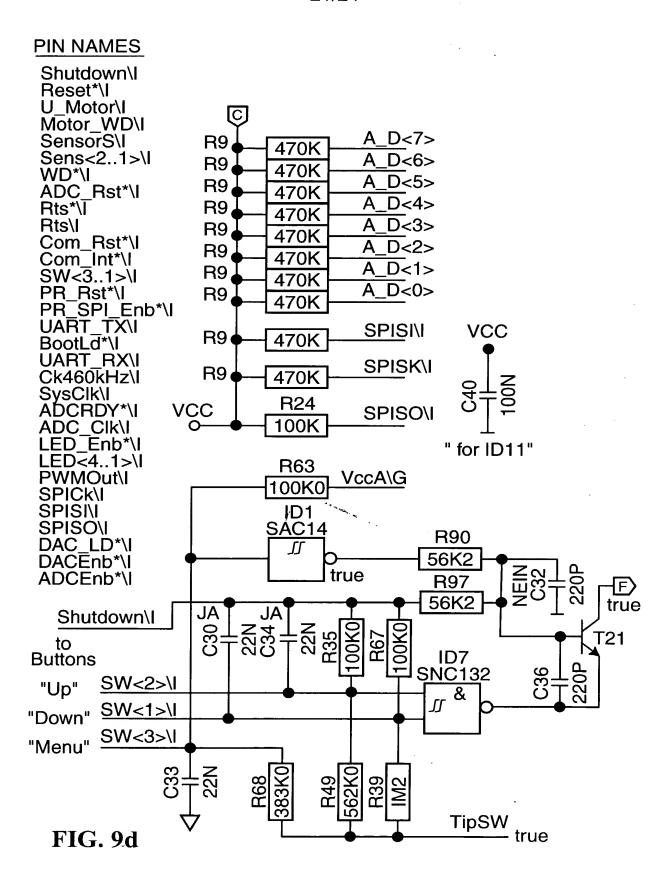
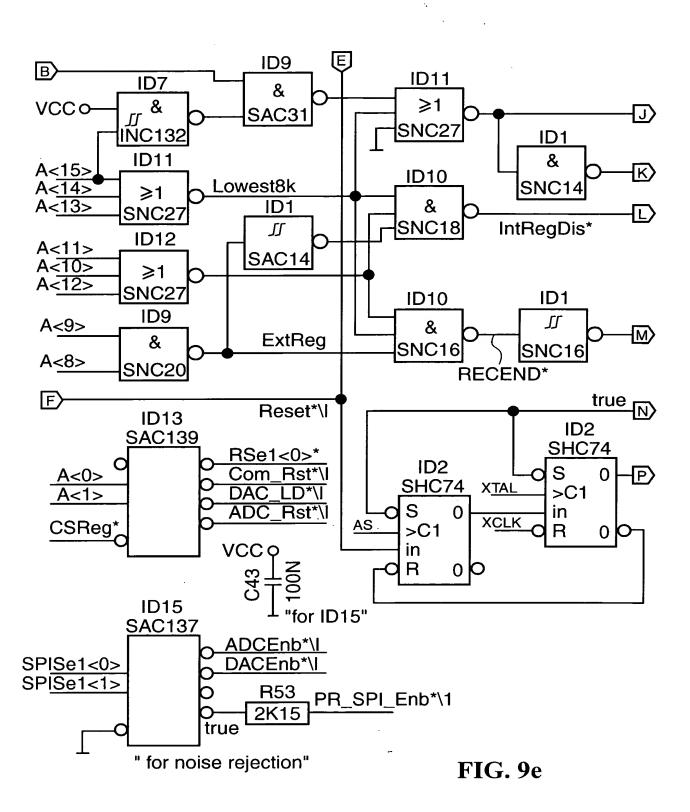


FIG. 9c

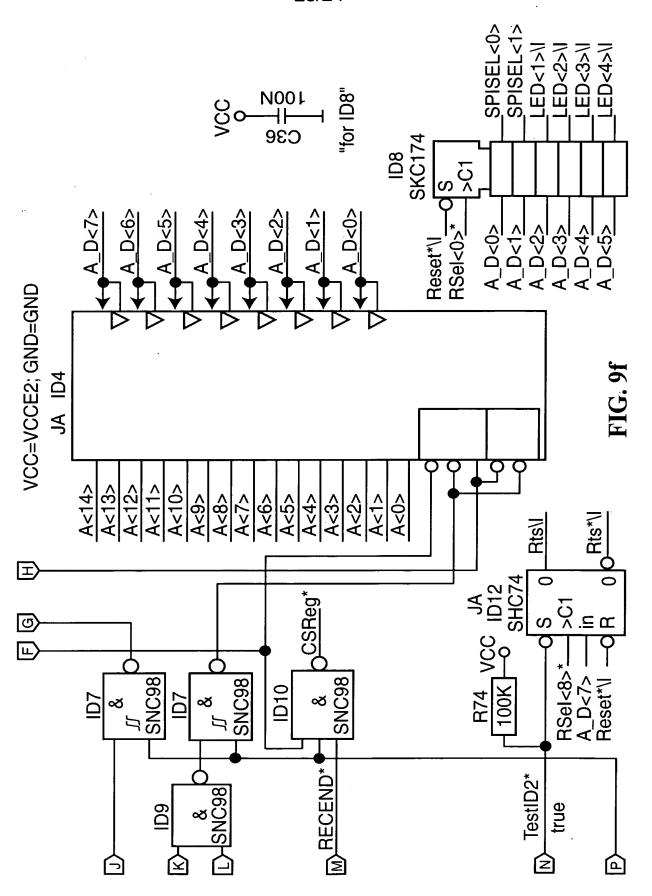








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